



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

Steven Derrick Clynes, et al.

Serial No: 09/862,523

Filed: May 22, 2001

Confirmation No: To be Assigned

For: METHOD OF ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS

Docket No: TI-32423

Art Unit: TBD

Examiner: TBD

Date: July 10, 2001

LETTER TO OFFICIAL DRAFTSPERSON

Assistant Commissioner For Patents
Washington, DC 20231

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

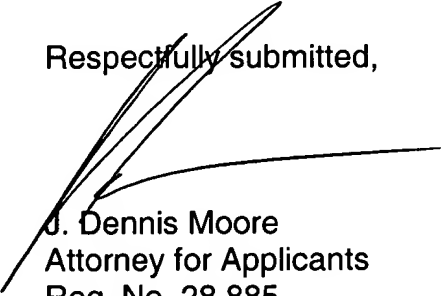
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 7-11-01.


Ann Trent

Dear Sir:

Transmitted herewith for filing are formal drawings for the above identified application. Charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,


J. Dennis Moore
Attorney for Applicants
Reg. No. 28,885

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